

RABIN & BERDO, P.C.

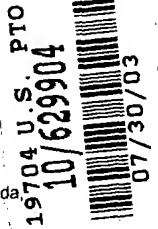
*Specializing in Patent, Trademark
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July 30, 2003

Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Attorney Reference: AI 289

Re: New Patent Application of: Kazutaka SHIBATA

Title: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Sir:

Please find attached hereto an application for patent which includes:

- ☒ Specification, Claims and Abstract (39 Pages)
- ☒ 6 Sheets of Formal Drawings (Fig. 1 through Fig. 6c)
- ☐ Inventor's Declaration and Power of Attorney (Page)
- ☐ Assignment document with cover page (Pages)
- ☒ Claim for priority

The rights of priority are claimed under 35 USC §119 of Japanese Application Nos. 2002-225097 and 2002-236036, filed August 1, 2002 and August 13, 2002, respectively (documents to be submitted later).

Respectfully submitted,

Robert H. Berdo, Jr. (Reg. No. 38,075)
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July 30, 2003

Date

RHB:tl